

PARISI RANI SWATHI SRI

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Career Objective

I am a Fresher Seeking to obtain a challenging and passionate career where I can Enhance my knowledge and utilize my skills towards the growth of the Organization while Ensuring my Self-growth with Team work and Extensive Leadership Qualities.

Academic Qualifications

Course	School/College	Board/University	Percentage/CGPA	Year
B.Tech (ECE)	Bapatla Engineering College	Acharya Nagarjuna University	9.19	2023
Intermediate	Vijaya Junior College	Board of Intermediate Education	9.69	2019
SSC	Vijaya High School	Board of SSC	9.5	2017

Work Experience

Graduate Apprentice Trainee - ISRO – ISTRAC - July 2025

- Developed a GUI-Based Monitoring & Control System for Satellite Tracking Receiver
- Assisted in monitoring and Operating satellite telemetry, tracking, and command systems at Ground Station.
- Working alongside Scientists on real-time satellite control activities.
- Supported Real-time Tracking of Satellites at Ground station.
- Gained Hands-on Experience with C#,TCP/IP, Socket Programming.

Technical Skills

- C Language
- C++ Language
- Embedded C
- TCP/IP
- C#
- Socket Programming
- Basics of Linux
- Protocols: UART, SPI, I2C

Projects

GUI-Based Monitoring & Control System for Satellite Tracking Receiver | C#, TCP/IP

Developed a C# GUI application to monitor and control a satellite tracking receiver using TCP/IP socket communication. Implemented real-time telemetry display and command interface for receiver status monitoring and control..

OTP Based Time Limited Access Control System (Embedded project)

The objective of this project is to design and develop an OTP-based time-limited access control system using GSM technology.

Design of an octapod for obstacle detection (Academic Project)

The objective of this project is to develop a spider robot i.e Octapod to overcome the problems in the places where it's impossible to move for any human.

Design of Dielectric Resonator loaded dual band Microstrip Antenna for 5G NR mm-wave Applications (Academic Project)

The objective of this project is to design Dielectric resonator mounted microstrip antenna to resonate at millimeter wave frequencies for 5G Applications.

Exposure

Completed Embedded Systems course at Vector India Institute.

Extra-Curricular Activities

- Participated in social activities like planting and other social services.
- Participated as a volunteer in college events.
- Took part as a member of AWAAZ in college.

Strengths

- Good communication skills
- Active listener
- Team Work
- Problem Solving
- Self-disciplined, self-confident and hardworking, optimistic in views

Personal Information

Father Name: Parisi Venkata Rajendra Kumar

Mother Name: Parisi Venkata Ratnamala

Date of Birth: 20/06/2002

Gender: Female

Nationality: Indian

Marital Status: Unmarried

Languages Known: Telugu, English

Current Address: 3-131, Shabul Bazaar, Challapalli, Krishna District, Andhra Pradesh-521126

Declaration

I hereby declare that the information stated above is true to the best of my knowledge.

Signature: P.R. SWATHI SRI