KARAN SINGH

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Websites, Portfolios, Profiles

- linkedin.com/in/exuberantkrn

Professional Summary

Proactive Electronics Engineer focused on exceeding corporate goals and continuous improvement. Logical and analytical professional certified in Cybersecurity paired with capacity to translate complex ideas into understandable concepts. Polished in testing theoretical designs, writing specifications and technical reports and creating user-friendly interfaces.

Skills

- Printed Circuit Board (PCB) Design
- Very-Large-Scale Integration (VLSI)
- VHDL
- Digital electronics
- Xilinx ISE
- Field-Programmable Gate Arrays (FPGA)
- Linux
- Risk Assessment
- Incident Response
- Project Management
- NIST framework
- Python
- SQL
- Data Security
- Disaster Recovery
- Information Protection
- Information Governance
- Security Planning
- Data Encryption
- Incident Response Management
- Project Planning

Work History

Electronic Engineer 03/2019 to 01/2024

J.C.T.Limited

With over four years of experience as an electronic engineer, I am passionate about applying my skills and knowledge in cybersecurity, information security, Linux, VHDL, and PCB design to create innovative and secure solutions for the industry. I currently work at J.C.T.Limited, a leading company in the field of textile. I share the company's vision of delivering high-quality products and services that meet the needs and expectations of our clients and stakeholders. I also value the company's culture of collaboration, learning, and diversity, and I enjoy being part of a team that brings together different perspectives and experiences to solve complex problems.

Industrial Training 01/2014 to 05/2014

Dream Tech Labs

- I have done my Industrial training in VLSI design using VHDL programming language on Xilinx Spartan 3E FPGA Development Board
- I did coding using various modeling methods such as Behavioral (if else statement, case statement, conditional signal assignment statement and selected signal assignment statement) and Structural modeling.
- My project was implementing Two way and Four way Traffic light controller using VHDL language on Xilinx Spartan 3E FPGA Development Board.

Project training 05/2012 to 06/2012

innovative project solutions

- I have done my six weeks practical training in VLSI and PCB designing.

Education

B.Tech, Electronics and Communication Engineering 07/2014

Punjab Technical University

Certifications

- Certified in Cybersecurity from ISC2
- Google Cybersecurity certified through Coursera
- Microsoft Cybersecurity Analyst certified through Coursera
- SIEM Splunk hands-on guide specialization through Coursera
- Google Cloud digital leader training certified through Coursera

Projects

Two way and Four way Traffic light controller, Implemented using VHDL language on Xilinx Spartan 3E FPGA Development Board