

DURGAPRASAD PISINI

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Professional Summary

To build a successful career in the field of Embedded Systems, leveraging my technical skills, academic knowledge, and passion for learning.

Skills

- Programming Languages: C, C++, Embedded C
 - Operating Systems: Linux, RTOS
 - Microcontrollers: 8051, ARM 7, ESP8266, ESP-01
 - Protocols: UART, I2C, SPI, CAN, LIN, UDS
 - Compilers: GCC, Turbo-C, Keil4
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Education

- **Dr. B. R. AMBEDKAR UNIVERSITY COLLEGE OF ENGINEERING, SRIKAKULAM**
BTECH, Electronics & Communication Engineering, 2023
CGPA: 79.50
 - **ANDHRA POLYTECHNIC, KAKINADA**
Diploma in Electrical & Communications Engineering, 2020
CGPA: 78.12
 - **Z.P HIGH SCHOOL**
Secondary School Certificate (S.S.C), 2017
CGPA: 93.0
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Training and Certification

- **Advanced Embedded Systems Training**
Vector India Private Ltd
Duration: November 24, 2023 - May 24, 2023
Description: Completed a 6-month Advanced Embedded Systems training program focusing on various aspects of embedded systems development, including hardware design, firmware development, and real-time operating systems.
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Projects

Wireless Notice Board Using Bluetooth, Arduino and NODEMCU 8266 WIFI Module

Description: Designed and implemented a versatile wireless notice board system leveraging Bluetooth and Wi-Fi technologies to enable remote message updates. This project showcases proficiency in embedded systems, wireless communication, and full-stack development.

Study and Analysis of ECG Signal Using MATLAB Simulator

Description: Understand the ECG signal, detect the r-peaks of each beat and calculate the heart beats per minute, Beat count of an ECG signal.

Vector Project

Can Protocol as a Real Time Application.

Description: Designed and executed a cutting-edge real-time communication system using the Controller Area Network (CAN) protocol, tailored for mission-critical applications in the automotive, industrial automation, and aerospace sectors. This project showcases expert-level proficiency in network protocol engineering, embedded system design, and real-time data processing.

Work Experience

COMPANY NAME : VILAS BUSINESS SOLUTIONS PVT LTD (VBS)
PROJECT NAME : INTEGRATED TRAILER CONTROL MODULE (ITCM)
ROLE : JR SYSTEM REQUIREMENTS ENGINEER
DURATION : Oct 2024 - Mar 2025
LOCATION : Hyderabad
TOOLS USED : JIRA, CRADLE.

Key Responsibilities:

- Defined and managed system-level requirements for the ITCM used in automotive trailer braking applications.
- Understanding ASPICE model and implemented them in Real time.
- Ensured end-to-end traceability using IBM CRADLE, and supported validation teams with requirement coverage analysis.
- Coordinated with hardware/software teams for alignment with CAN protocol-based communication.

COMPANY NAME : LAB CHILD SOLUTIONS
PROJECT NAME : Wi-Fi Enabled Digital Clock using Low power at Prototype Stage
ROLE : HARDWARE ENGINEER
DURATION : APR 2025 – JUN 2025
LOCATION : MANNEGUDA, HYDERABAD
TOOLS USED/TECH USED: ESP8266, OLED DISPLAY, RTC(DS3231).

Key Responsibilities:

- **SOLDERING FOR PCB DESIGN.**
 - **DESIGNEING THE SCHMATIC DIAGRAM FOR THE PROJECT.**
 - **UNDERSTAND THE SPI PROTOCOL.**
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Certifications

- System design through Verilog.
 - CCNA Certification
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Personal Information

- **Date of Birth:** June 3, 2002
 - **Hobbies:** playing cricket, Spending time with old people.
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Extra-Curricular Activities

- Participation in U-19 Cricket Selection.
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Declaration

I hereby declare that the above written particulars are true and correct to the best of my knowledge and belief.

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